

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

Inventors: Nicholas D. Signore and Curtis A. Wickman
Title: METHOD AND APPARATUS FOR DETERMINING THE OPTIMAL FANOUT ACROSS A LOGIC ELEMENT
Filing Date: 11/3/2003
Serial Number: Unknown
Group Art Unit: Unknown
Examiner: Unknown

On an enclosed Form PTO-1449 is information known to applicant(s) and submitted pursuant to 37 C.F.R. § 1.56. A copy of each listed publication and U.S. and foreign patent, except for pending U.S. applications, is being submitted herewith, along with a concise explanation of information in a foreign language, if any, pursuant to 37 C.F.R. § 1.97-1.98.

Applicants respectfully request that the listed information be considered by the Examiner and be made of record in the above-identified application. The Examiner is requested to initial and return the enclosed Form PTO-1449 in accordance with MPEP § 609.

This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in § 1.56.

This statement qualifies under 37 C.F.R. § 1.97, subsection (b) because (check all that apply):

(1) It is being filed within 3 months of the application filing date
-- OR --

(2) It is being filed within 3 months of entry of a national stage
-- OR --

(3) It is being filed before the mail date of the first Office Action on the merits.

37 C.F.R. § 1.97(c). If this statement is being filed after the latest of: (1) three months beyond the filing date of a national application; (2) three months beyond the date of entry of the national stage as set forth in § 1.491 in an international application; or (3) the mailing date of a first Office action on the merits, but before the mailing date of the earlier of a final office action under § 1.113 or a notice of allowance under § 1.311, then:

a certification as specified in § 1.97(e) is provided below; or

a fee of \$240.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement. Please note that a check in the amount of \$240.00 is enclosed in payment.

37 C.F.R. § 1.97(d). If this statement is being filed after the mailing date of the earlier of a final office action under § 1.113 or a notice of allowance under § 1.311, but before payment of the issue fee, then:

A. a certification as specified in § 1.97(e) is completed below; and

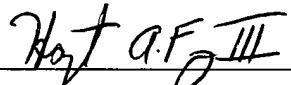
- B. a petition under 37 C.F.R. § 1.97(d) requesting consideration of this statement is submitted herewith; and
- C. a fee of \$130.00 as set forth in § 1.17(i)(1) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.

Statement under 37 C.F.R. §1.97(e) - I hereby certify that either: each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three months prior to the filing of the information disclosure statement; or no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign patent application, and, to the knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement.

Respectfully submitted,

Name: Hoyt A. Fleming III
Reg. No.: 41,752

Signature:



Date: 11/3/2003

Park, Vaughan & Fleming LLP
P.O. Box 140678
Boise, ID 83714
(208) 336-5237

28422

INFORMATION DISCLOSURE CITATION

PTO-1449

C mplete if Known

Application No.	Unknown
Filing Date	November 3, 2003
First Named Inventor	Nicholas D. Signore
Group Art Unit	Unknown
Atty. Docket No.	SUN-P9728-MEG

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	6,629,301	Sep. 30, 2003	Sutherland et al.	716	8	Sep. 15, 2000

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
						<input type="checkbox"/>	<input type="checkbox"/>
						<input type="checkbox"/>	<input type="checkbox"/>
						<input type="checkbox"/>	<input type="checkbox"/>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Sutherland et al., Logical Effort Designing Fast CMOS Circuits, 1999

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.